		·		
L Number	Hits	Search Text	DB	Time stamp
1	22	(master\$3 adj (processor\$5 computer\$5	USPAT	2003/05/30 12:34
		system\$3 microprocessor\$5 cpu)) with		
		((read\$5 writ\$6) adj request\$5)		
2	223	<pre>master\$3 with ((read\$5 and writ\$6)</pre>	USPAT	2003/05/30 12:58
		near2 request\$5)		
4	2024	(memory adj access adj (instruction\$5	USPAT	2003/05/30 13:01
-		request\$6))		
5	544	(memory adj access adj (instruction\$5))	USPAT	2003/05/30 13:05
6	170	(memory adj access adj (instruction\$5))	USPAT	2003/05/30 13:06
0	1,0	with (load store read write)		
10	0	6363444.pn. and (bridge\$5 with bus\$6)	USPAT	2003/05/30 13:30
11	1	6363444.pn. and (bus\$3)	USPAT	2003/05/30 13:32
12	29	(slave) with integrat\$5 with bridge	USPAT	2003/05/30 13:35
12	476	(slave) with bridge\$3	USPAT	2003/05/30 13:36
13	53	(slave) with bridge\$3 with (integrat\$6	USPAT	2003/05/30 13:36
14	55	single))	ODIAI	2003/03/30 13.30
16	5		USPAT	2003/05/30 13:39
15	53	(slave with bridge\$3 with (integrat\$6	USPAI	2003/03/30 13:39
	176	embed\$6 single))	UODAM	2002/05/20 12:20
16	476	(slave with bridge\$3)	USPAT	2003/05/30 13:39
17	14	(slave\$5 adj (processor\$5 computer\$5	USPAT	2003/05/30 13:45
		system\$3 microprocessor\$5 cpu)) with		
		bridg\$5		
18	1	6363444.pn. and (program\$5 instruction\$5	USPAT	2003/05/30 13:48
		code\$3)		
19	30	((master\$3 slave\$5) adj3 (processor\$5)	USPAT	2003/05/30 13:50
		<pre>computer\$5 system\$3 microprocessor\$5 cpu))</pre>		
		with ((memory adj access adj		
		(instruction\$5 request\$6)))		
20	41	((master\$3 slave\$5) adj3 (processor\$5	USPAT	2003/05/30 13:55
20		computer\$5 system\$3 microprocessor\$5 cpu))		
		same ((memory adj access adj		
		(instruction\$5 request\$6)))		
28	5	slave with bridge\$5 with (on-chip\$3 (on	USPAT	2003/05/30 15:02
20	5	near chip\$3))	ODIMI	
_	12785	(master\$3 slave\$5) adj3 (processor\$5	USPAT	2003/05/30 13:40
-	12705	computer\$5 system\$3 microprocessor\$5 cpu)	USPAI	2005/05/50 15.40
	1.5.50		ПОРАЛ	2003/05/30 13:49
-	1558	((master\$3 slave\$5) adj3 (processor\$5	USPAT	2003/05/30 13:49
		<pre>computer\$5 system\$3 microprocessor\$5 cpu))</pre>		
		with (read\$5 writ\$6)		
-	10	4853847.uref.	USPAT	2003/05/29 13:44
-	1	6363444.pn. and (master slave)	USPAT	2003/05/29 13:47
-	0	6363444.uref.	USPAT	2003/05/29 13:47
-	1	(butterworth near henry near e).in.	USPAT;	2003/05/29 14:03
		(fuente near carlos near f).in. (nicholson	US-PGPUB;	
		near robert near b).in.	EPO; JPO;	
			DERWENT;	
		·	IBM TDB	
-	15	((master\$3 and slave\$5) adj (processor\$5	USPAT	2003/05/29 14:12
		<pre>computer\$5 system\$3 microprocessor\$5 cpu))</pre>		
		with (memory near2 instruction\$3)		
-	254	((master\$3 and slave\$5) adj (processor\$5	USPAT	2003/05/29 16:48
		computer\$5 system\$3 microprocessor\$5 cpu))		
		with (memory)		
_	485	(non-uniform near memory near	USPAT	2003/05/29 16:48
		architecture) numa		
_	125	((non-uniform near memory near	USPAT	2003/05/29 16:53
	123	architecture) numa) and (master slave)	JULIA	2003/03/29 10:33
	1		UCDAM	2003/05/29 16:51
-	1	((non-uniform near memory near	USPAT	2003/03/29 10:31
		architecture) numa) and 709/208		
-	0	((non-uniform near memory near	USPAT	2003/05/29 16:51
		architecture) numa) and 709/208.ccls.		
-	80	((non-uniform near memory near	USPAT	2003/05/29 16:54
		architecture) numa) and (international adj		
		business).as.		
-	41	(((non-uniform near memory near	USPAT	2003/05/29 17:06
		architecture) numa) and (master slave))		
		and (international adj business).as.		
			USPAT	2003/05/29 17:13
_	3	(("60/8665") Or ("5/52068") or	USPAI	
-	3	(("6078665") or ("5752068") or ("5710939")).PN.	USPAI	2003/03/23 11:13
-		("5710939")).PN.		
-	3 0 0		USPAT USPAT USPAT	2003/05/29 17:14 2003/05/29 17:14

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. . .

-	0	4853847.pn. and (buffer cache)	USPAT	2003/05/29 17:15
-		(mitsurou near ohuchi).in.	USPAT	2003/05/29 17:32
-	1	6363444.pn. and (request\$6)	USPAT	2003/05/29 17:32

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